

ESD8V0D3

Description

ESD8V0D3 is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium



Features

- Ultra low leakage: nA level
- Operating voltage: 8V
- Package: SOD-323
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

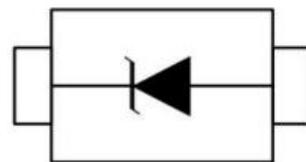
Air discharge: $\pm 15\text{kV}$

Contact discharge: $\pm 8\text{kV}$

– IEC61000-4-4 (EFT) 40A (5/50ns)

– IEC61000-4-5 (Lightning) 12A (8/20 μs)

Functional Diagram



Applications

- Cell Phone Handsets and Accessories
- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Peripherals
- Pagers

Absolute Maximum Ratings(Tamb=25°C unless otherwise specified)

| Parameter | Symbol | Value | Unit |
|--|------------------|--------------|-------|
| Peak Pulse Power (8/20 μs) | P _{PP} | 350 | Watts |
| ESD per IEC 61000-4-2 (Air) | V _{ESD} | ± 15 | KV |
| ESD per IEC 61000-4-2 (Contact) | | ± 8 | KV |
| Lead Soldering Temperature | T _L | 260 (10 sec) | °C |
| Operating Temperature Range | T _J | -55 to +150 | °C |
| Storage Temperature Range | T _{STJ} | -55 to +150 | °C |

Electrical Characteristics (TA = 25 °C unless otherwise noted)

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Units |
|---------------------------|------------------|---|------|------|------|-------|
| Reverse Stand-off Voltage | V _{RWM} | | | | 8 | V |
| Reverse Breakdown Voltage | V _{BR} | I _L = 1mA | 8.5 | | | V |
| Reverse Leakage Current | I _R | V _R = V _{RWM} | | | 10 | µA |
| Clamping Voltage | V _C | I _{PP} =1A, t _P = 8/20µs | | | 13.4 | V |
| | | I _{PP} =18A, t _P = 8/20µs | | | 21 | V |
| Junction Capacitance | C _J | V _R =0V, f = 1MHz | | | 150 | pF |

Characteristics Curves

Figure 1- Power Derating Curve

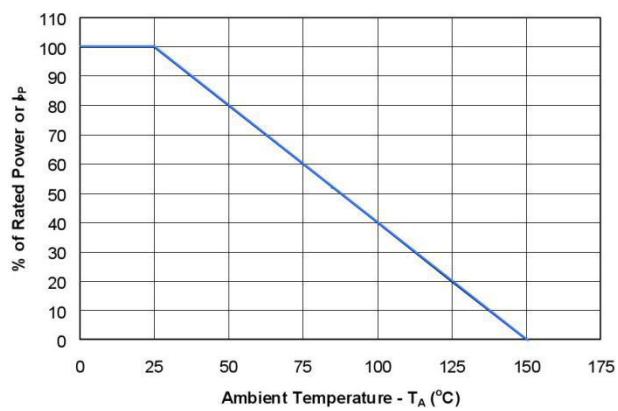
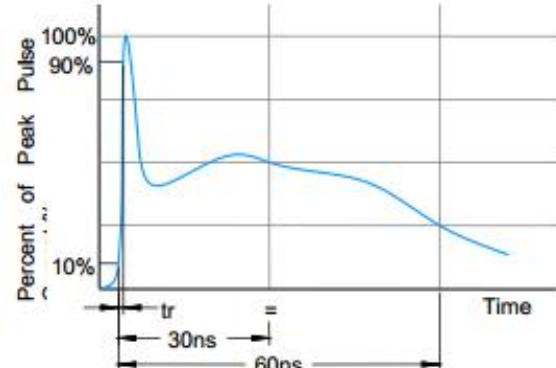
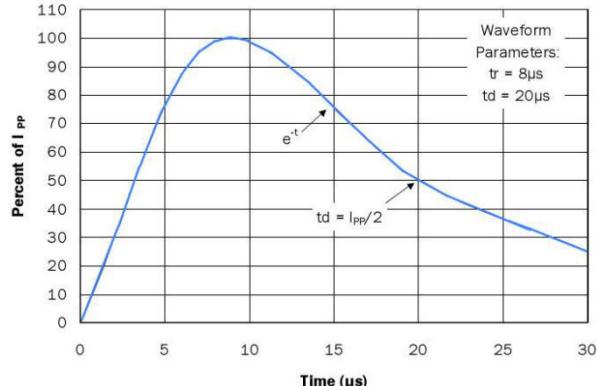
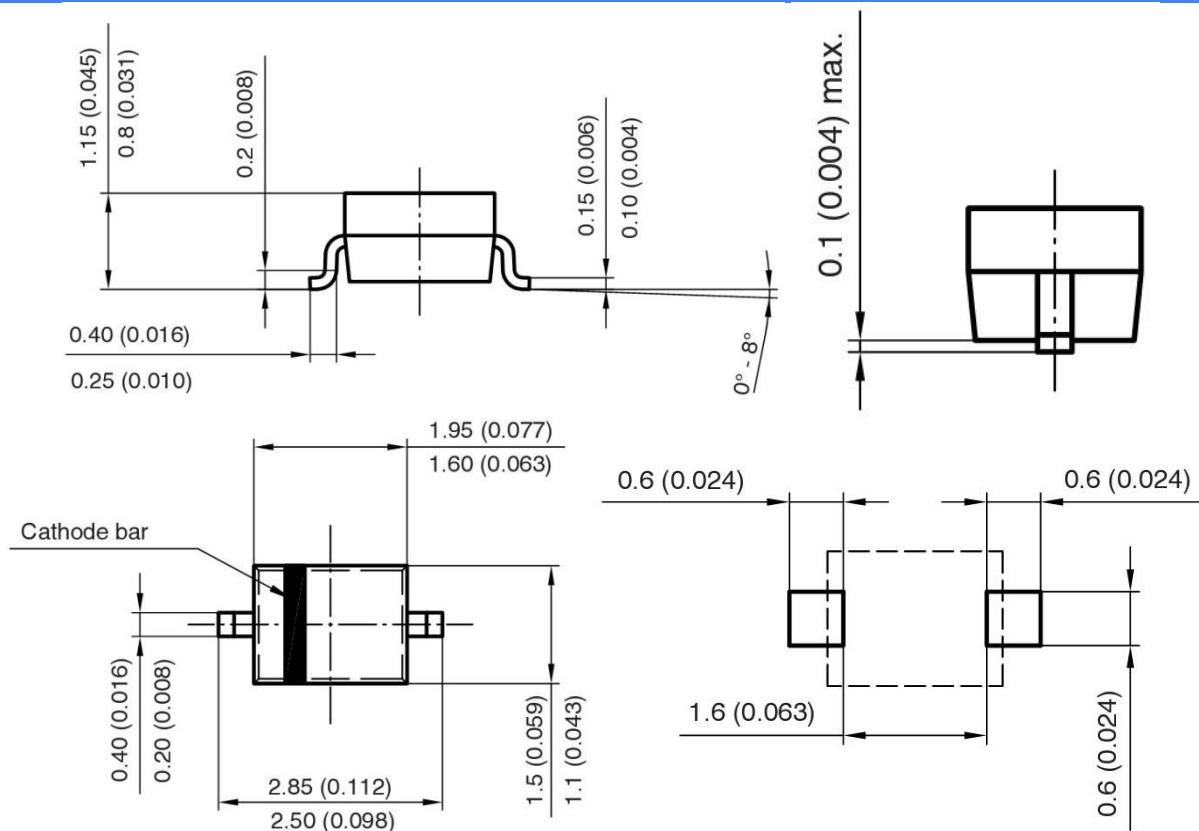

 Figure 2- ESD Pulse Waveform
(according to IEC 61000-4-2)


Figure3- 8/20µs Pulse Waveform



PACKAGE OUTLINE DIMENSIONS in millimeters (inches) :SOD323



Mounting Pad Layout

Disclaimer

Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.

Users should verify actual device performance in their specific applications.